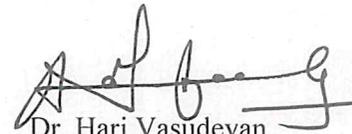# RE-EXAMINATION TIMETABLE, MARCH, 2026
BACHELOR OF TECHNOLOGY (B.Tech.)

THIRD YEAR, SEMESTER-V (for 2024-25 Batch Under Autonomy)

Timings: 10:00 a.m. to 12:00 p.m.

Date: 20th February, 2026

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engineering	Information Technology
Tuesday, 17/03/2026	Dynamics of Machinery	Radio Frequency Circuit Design	Data Warehousing and Mining
Friday, 20/03/2026	Incubation, Entrepreneurship and Start-ups	Digital Signal Processing	Artificial Intelligence
Tuesday, 24/03/2026	Heat Transfer	Analog Communication	Advanced Data Structures
Friday, 27/03/2026	Industrial Electronics	Microcontroller & Applications-II	Cryptography and Network Security
Monday, 30/03/2026	Automotive Prime Movers	-	Computer Graphics

Important Note: Change if any, in the time table shall be communicated on the College website (www.djsce.ac.in).

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal